

Power Inductor

Automotive Grade

AFSI Series



Overview

Power inductors are passive electronic components used in various circuits to store energy in a magnetic field when electrical current flows through them. They are critical in filtering, energy storage, and noise suppression in power electronic systems. They are designed to handle higher currents and are optimized for minimal power loss and thermal efficiency.

Benefits

1. Automotive grade available
2. Large size bottom termination
3. Low DCR
4. Large current

Applications

1. Automotive Systems
2. VRM/VRD
3. Industrial Control

Product Information

Series	L (mm)	W(mm)	T (mm)	Inductance (μH)
AFSI	9.3	9.0	8.1	0.45 ~ 33
	9.3	9.0	8.1	
	12.0	11.0	8.8	
	11.8	10.5	10.0	
	16.3	15.3	8.0	
	16.3	15.3	10.0	

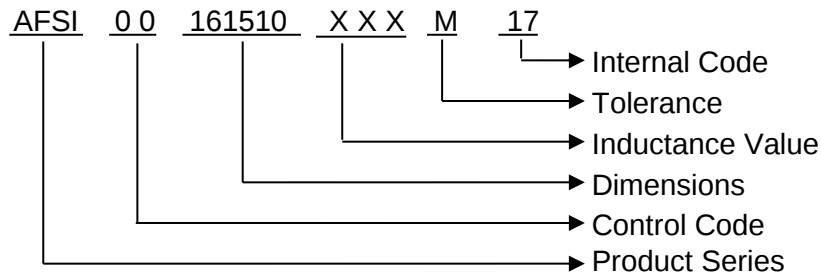


AFSI00161510XXXM17 Series Specification

AEC-Q200

1 Scope: This specification applies to large current and low RDC SMD power inductor.

2 Part Numbering:

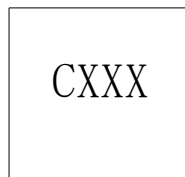


3 Rating:

Operating temperature range: $-50^{\circ}\text{C} \sim 150^{\circ}\text{C}$ (Including self - temperature rise)

Storage condition: $-20^{\circ}\text{C} \sim 40^{\circ}\text{C}$; 75%RH Max

4 Marking:



Marking color : Black

Marking : C → Chilisin
XXX → Inductance

5 Material List:

NO	Part	Material
1	Copper Wire	EIAIW (200°C)
2	Core	Carbonyl iron powder
3	coating	Paint Gray (127# matte)
4	Solder	Sn96.5-Ag3.0-Cu0.5
5	Ink	jet ink black (9210)
6	EPOXY	M02-039-AU(6461HF)

6 Standard Testing Condition

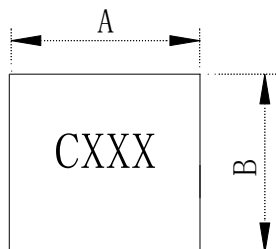
	Unless otherwise specified	In case of doubt
Temperature	Ordinary Temperature(15 to 35°C)	20 to 30°C
Humidity	Ordinary Humidity(25 to 85% RH)	50 to 80% RH

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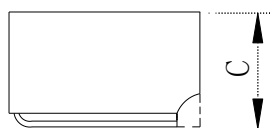
AEC-Q200

7 Configuration and Dimensions:

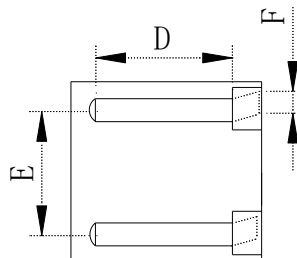
TOP VIEW



SIDE VIEW



BOTTOM VIEW



Product appearance



Part No.	A	B	C	D	E	F
AFSI001615104R7M17	16.3±0.5	15.3±0.5	10.0Max	12.0±0.3	10.5±0.5	1.8±0.1
AFSI001615106R8M17	16.3±0.5	15.3±0.5	10.0Max	12.0±0.3	10.5±0.5	1.8±0.1
AFSI001615108R2M17	16.3±0.5	15.3±0.5	10.0Max	12.0±0.3	10.5±0.5	1.8±0.1
AFSI00161510100M17	16.3±0.5	15.3±0.5	10.0Max	12.0±0.3	10.5±0.5	1.8±0.1
AFSI00161510150M17	16.3±0.5	15.3±0.5	10.0Max	12.0±0.3	10.5±0.5	1.8±0.1
AFSI00161510220M17	16.3±0.5	15.3±0.5	10.0Max	12.0±0.3	10.5±0.5	1.8±0.1
AFSI00161510330M17	16.3±0.5	15.3±0.5	10.0Max	12.0±0.3	10.5±0.5	1.8±0.1

8 Electrical Characteristics:

Part No.	Inductance (uH)	Test Freq.	Tolerance (±%)	I _{rms} (A)Typ.	I _{sat} (A)Typ.	RDC (mΩ)Max	Marking
AFSI001615104R7M17	4.7	100kHz,1.0V	20	30	60	3.8(3.1typ)	C4R7
AFSI001615106R8M17	6.8	100kHz,1.0V	20	26	50	4.6(4.1typ)	C6R8
AFSI001615108R2M17	8.2	100kHz,1.0V	20	25	43	6.0(4.7typ)	C8R2
AFSI00161510100M17	10	100kHz,1.0V	20	22	40	8.0(6.6typ)	C100
AFSI00161510150M17	15	100kHz,1.0V	20	18	32	11.4(9.6typ)	C150
AFSI00161510220M17	22	100kHz,1.0V	20	15	28	14(12.4typ)	C220
AFSI00161510330M17	33	100kHz,1.0V	20	12	24	19(17.9typ)	C330

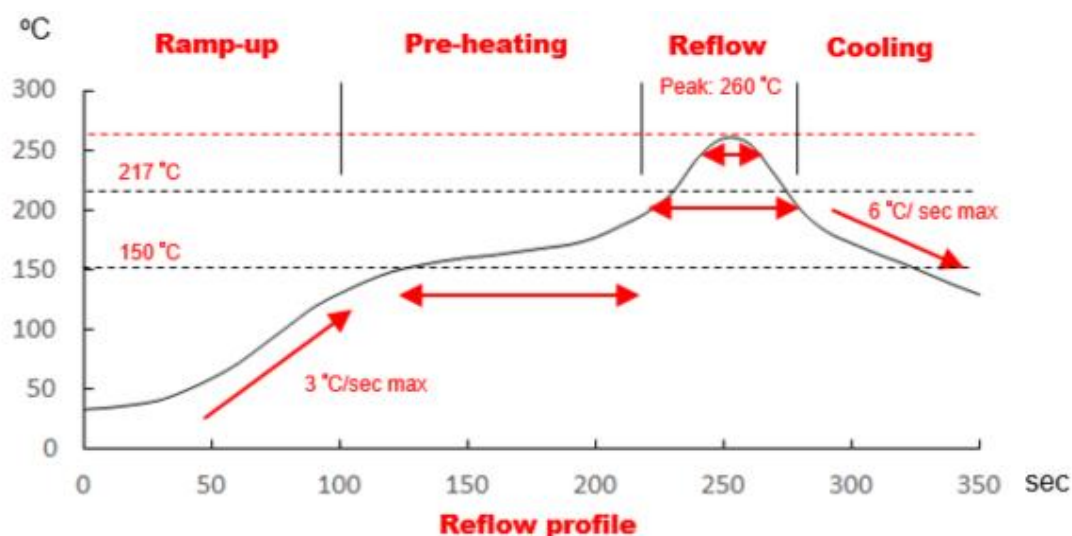
Note:

- 1.Ambient + temperature rise range — 50°C~150°C(Including self - temperature rise)
- 2.I_{sat} for Inductance drop 30% from its value without current.
- 3.I_{rms} for a 40°C temprature rise from 25°C ambient.
- 4.The part temperature (ambient + temp rise) should not exceed 150°C under worst case operating conditions.
Circuit design150°C under worst case operating conditions. Component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 5.Before loading the parts, glue in the middle of the corresponding PCB Layout size to consolidate the parts to prevent Reflow from causing displacement problems

9 Reliability verification conditions

No.	Stress	Reference	Test Conditions	Sample Size	Specification	Result
1	High Temperature Exposure (Storage)	MIL-STD-202 Method 108	1000hrs. at rated operating temperature, part can be stored for 1000 hrs. @ 150°C. Unpowered. Measurement at 24±4 hours after test conclusion.	77	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS
2	Temperature Cycling	JESD22 Method JA-104	1000 cycles (-50°C to +150°C). Measurement at 24±4 hours after test conclusion. 30min maximum dwell time at each temperature extreme. 1 min. maximum transition time.	77	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS
3	Biased Humidity	MIL-STD-202 Method 103	preconditioning : reflow 3 times ;Temperature:85°C ;Humidity:85%;Time:1000hrs;Unpowered. Measurement at 24±4 hours after test conclusion.	77	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS
4	Operational Life	MIL-PRF-27	1000 hrs. @ 150 °C. @Applied Current: Rated Current .Measurement 24±4 hours after test conclusion.	77	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS
5	Physical Dimension	JESD22 Method JB-100	Verify physical dimensions to the applicable device detail specification.	30	Product spec.	PASS
6	Resistance to Solvents	MIL-STD-202 Method 215	Immersion 3+0.5/-0 minutes in Terpene defluxer. Brush 10 strokes (wet bristle) 2 to 3 oz. Rinse in water. Air blow dry.	5	1. Marking resistance to solvent-No constitute failure (≤3X magnification) 2. Component protective coating, encapsulation material and sleeve material resistance - No damage or degradation that has occurred due to solvent (10X magnification)	PASS
7	Mechanical Shock	MIL-STD-202 Method 213	Units are non-operating. Pulse shape : Half-sine waveform Impact acceleration : 100 g's Pulse duration : 6 ms Number of shocks : 18 shocks (3 shocks for each face)	30	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS
8	Vibration	MIL-STD-202 Method 204	Pulse Shape:sine wave Amplitude :5g Sweep time and duration:10Hz to 2000Hz to 10Hz in 20minutes as a period Number of Vibrations :12 cycles each of 3 orientations.	30	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS
10	Solderability	J-STD-002	Method B1 : 1 - preconditioning : 8hours steam age test 2 - Solder temperature 245±5°C ,time 5.0±0.5sec. 3 - Speed: 25±6mm/s. Method D : 1 - preconditioning : 8hours steam age test 2 - Solder temperature 260±5°C ,time 30+5/-0sec. 3 - Speed: 25±6mm/s.	15	All terminations shall exhibit a continuous solder coating free from defects for a minimum of 95% of the critical area of any individual termination. (10X magnification)	PASS
11	Board Flex	AEC-Q200-005	Bend the board (D) X =2mm, 60sec minimum holding time.	30	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS
12	Terminal Strength (SMD)	AEC-Q200-006	Apply a 1.8Kg force to the side of a device bending tested. The force shall be applied for 60+1 seconds.	30	1. Electrical characterization-Inductance change range 2. Appearance-No damage (OM)	PASS

10 Reflow Profile:



Lead-Free(LF)

Refer to J-STD-020F

Item	Ramp-up	Pre-heating	Reflow	Peak Temp.	Cooling
Temp. scope	R.T. ~150 °C	150 °C~200 °C	217 °C	260±5 °C	Peak Temp. 150 °C
Time spec	-	60~120 sec	60~150 sec	20~40 sec	-

NOTE:

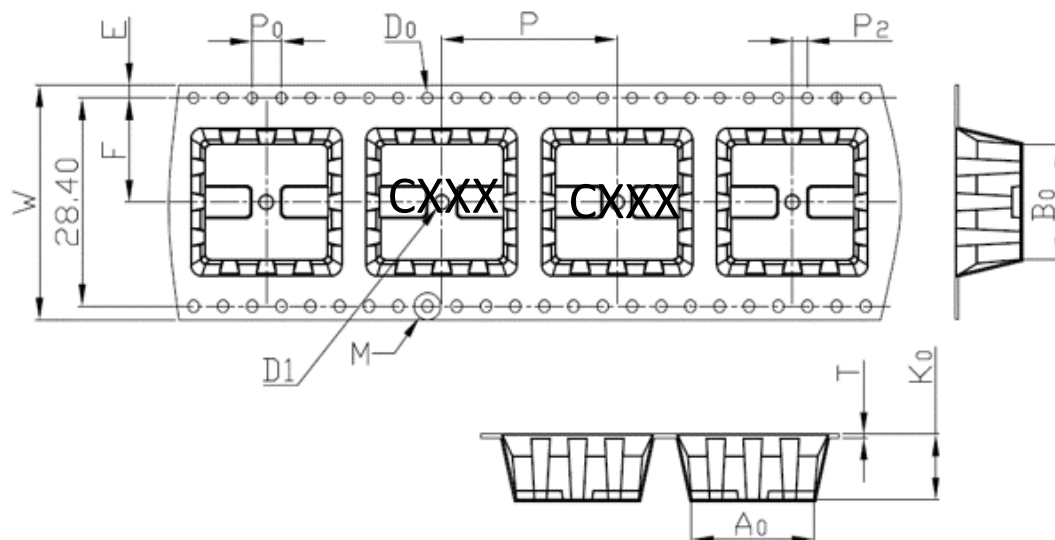
1. Re-flow possible times : within 2 times
2. Nitrogen adopted is recommended while in re-flow

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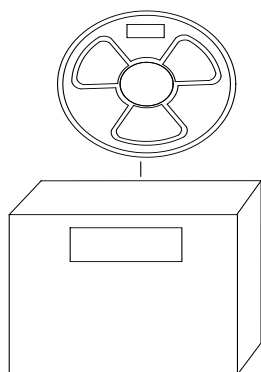
11 Packaging:

11.1 Small Pack: 150 pcs per reel



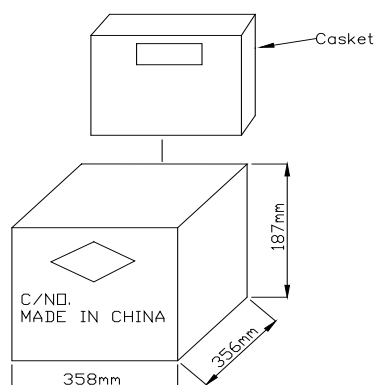
项目	W	A ₀	B ₀	K ₀	K ₁	P	F	E	D ₀	D ₁	P ₀	P ₂	T
尺寸	32.00 ^{+0.30} _{-0.30}	16.75 ^{±0.10}	15.70 ^{±0.10}	10.50 ^{±0.10}		24.00 ^{±0.10}	14.20 ^{±0.10}	1.75 ^{±0.10}	1.50 ^{+0.10} _{-0.00}	2.00 ^{+0.10} _{-0.00}	4.00 ^{±0.10}	2.00 ^{±0.10}	0.50 ^{±0.05}

11.2 Medium pack: 2 reel per casket (300 pcs)



Inner carton number	8401-013-01
Carrier	M30-021-886-0
Cover	M30-010-36-1
Reel	M34-006-05
Desiccant	M66.002
pouch	M32-002-09
Label type	Self-adhesion

11.3 Large pack: 2 small carton per carton (600pcs)



Carton number	8402-049-01
Label location	Top left corner on side surface
Label size	
Label type	Self-adhesion

13 Graph :

