

C1812C474J5JACTU

Aliases (C1812C474J5JAC7800)

SMD Comm U2J, Ceramic, 0.47 uF, 5%, 50 VDC, U2J, SMD, MLCC, Ultra-Stable, Low Loss, Class I, 2.3 mm, 1812 / 4532



Click [here](#) for the 3D model.

General Information

Series	SMD Comm U2J
Style	SMD Chip
Description	SMD, MLCC, Ultra-Stable, Low Loss, Class I
Features	Ultra-Stable, Low Loss, Class I
RoHS	Yes
Termination	Tin
Marking	No
Typical Component Weight	87 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	0.47 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	U2J
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	-750+/-120 ppm/C, 1kHz 1.0Vrms
Dissipation Factor	0.1% 1 kHz 1.0Vrms
Aging Rate	0.1% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	2.1277 GOhms

Dimensions

L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
T	1.7mm +/-0.20mm
S	2.3mm MIN
B	0.6mm +/-0.35mm
Case Code (EIA / mm)	1812 / 4532

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	1000

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