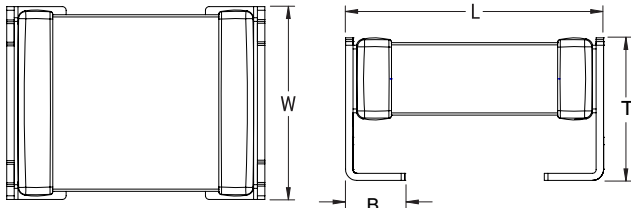


TOP VIEW
Single or Double Chip Stack

PROFILE VIEW
Single Chip Stack



Click [here](#) for the 3D model.

General Information

Series	KPS SMD Comm X7R
Style	Stacked Chip
Description	SMD, MLCC, Stacked, Single Chip, Temperature Stable
Features	Temperature Stable
RoHS	Yes
Termination	Tin
Typical Component Weight	170 mg
Shelf Life	78 Weeks
MSL	1

Dimensions

L	5mm +/-0.5mm
W	3.5mm +/-0.5mm
T	2.65mm +/-0.35mm
B	1.1mm +/-0.3mm

Packaging Specifications

Packaging	T&R, 330mm, Plastic Tape
Packaging Quantity	2000

Specifications

Capacitance	0.22 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	20%
Voltage DC	250 VDC
Dielectric Withstanding Voltage	625 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	4.5455 GOhms

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