

C1210C685K4PACTU

Aliases (C1210C685K4PAC7800)

SMD Comm X5R, Ceramic, 6.8 uF, 10%, 16 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1.5 mm, 1210 / 3225



Click [here](#) for the 3D model.

General Information

Series	SMD Comm X5R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
Typical Component Weight	105 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	6.8 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	10%
Voltage DC	16 VDC
Dielectric Withstanding Voltage	40 VDC
Temperature Range	-55/+85°C
Temp. Coefficient	X5R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	10% 1kHz 1.0Vrms
Aging Rate	5% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	14.7 MOhms

Dimensions

L	3.2mm +/-0.3mm
W	2.5mm +/-0.22mm
T	1.7mm +/-0.2mm
S	1.5mm MIN
B	0.5mm +/-0.25mm
Case Code (EIA / mm)	1210 / 3225

Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	2000

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