

C0805C105K4PACTU

Aliases (C0805C105K4PAC7800)

SMD Comm X5R, Ceramic, 1uF, 10%, 16 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 0.7 mm, 0805 / 2012



Click [here](#) for the 3D model.

General Information

| | |
|--------------------------|---|
| Series | SMD Comm X5R |
| Style | SMD Chip |
| Description | SMD, MLCC, Temperature Stable, Class II |
| Features | Temperature Stable, Class II |
| RoHS | Yes |
| Termination | Tin |
| Marking | No |
| Typical Component Weight | 13 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

Specifications

| | |
|--|---|
| Capacitance | 1uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Tolerance | 10% |
| Voltage DC | 16 VDC |
| Dielectric Withstanding Voltage | 40 VDC |
| Temperature Range | -55/+85°C |
| Temp. Coefficient | X5R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms |
| Dissipation Factor | 10% 1kHz 1.0Vrms |
| Aging Rate | 5% Loss/Decade Hour: Referee Time is 48 Hours |
| Insulation Resistance | 100 MOhms |

Dimensions

| | |
|----------------------|-----------------|
| L | 2mm +/-0.2mm |
| W | 1.25mm +/-0.2mm |
| T | 1.25mm +/-0.2mm |
| S | 0.7mm MIN |
| B | 0.5mm +/-0.25mm |
| Case Code (EIA / mm) | 0805 / 2012 |

Packaging Specifications

| | |
|--------------------|------------------------|
| Packaging | T&R, 180mm, Paper Tape |
| Packaging Quantity | 2500 |

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