

C0805C103J5RECTU

Aliases (C0805C103J5REC7800)

ESD SMD Comm X7R, Ceramic, 0.01 uF, 5%, 50 VDC, X7R, SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II, 0.7 mm, 0805 / 2012



Click [here](#) for the 3D model.

General Information

Series	ESD SMD Comm X7R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Electro Static Discharge, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
Typical Component Weight	11 mg
Shelf Life	78 Weeks
MSL	1

Specifications

Capacitance	0.01 uF
Measurement Condition	1 kHz 1.0Vrms
Tolerance	5%
Voltage DC	50 VDC
ESD Level per AEC-Q200	25,000 V ESD Level
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temp. Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	100 GOhms

Dimensions

L	2mm +/-0.2mm
W	1.25mm +/-0.2mm
T	0.78mm +/-0.10mm
S	0.7mm MIN
B	0.5mm +/-0.25mm
Case Code (EIA / mm)	0805 / 2012

Packaging Specifications

Packaging	T&R, 180mm, Paper Tape
Packaging Quantity	4000

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