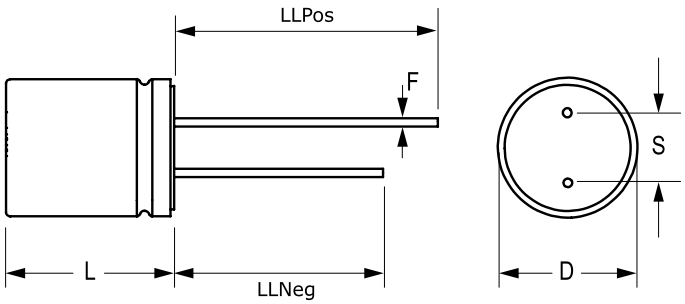
A750MS567M1EAAE015

A750, Polymer Aluminum, 560 uF, 20%, 25 VDC, -55/+105°C, 5 mm



Click here for the 3D model.

General Information	
Series	A750
Dielectric	Polymer Aluminum
Description	Single Ended, Polymer Aluminum
RoHS	Yes
Lead	Wire Leads
Halogen Free	Yes

Dimensions	
D	10mm +/-0.5mm
L	12mm +/-1mm
S	5mm +/-0.5mm
LL Negative	15mm MIN
LL Positive	19mm MIN
F	0.6mm +/-0.05mm

Packaging Specifications	
Packaging	Bulk, Bag

Specifications	
Capacitance	560 uF
Tolerance	20%
Voltage DC	25 VDC, 28.8 VDC (Surge)
Temperature Range	-55/+105°C
Rated Temperature	105°C
Life	2000 Hrs (+/-20% Initial Capacitance, 1.5x DF MAX, Leakage Within Limit)
Dissipation Factor	12% 120Hz 20C
ESR	15 mOhms (100kHz 20C)
Ripple Current	4900 mAmps (100kHz 105C)
Leakage Current	2800 uA (2min 20°C)

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

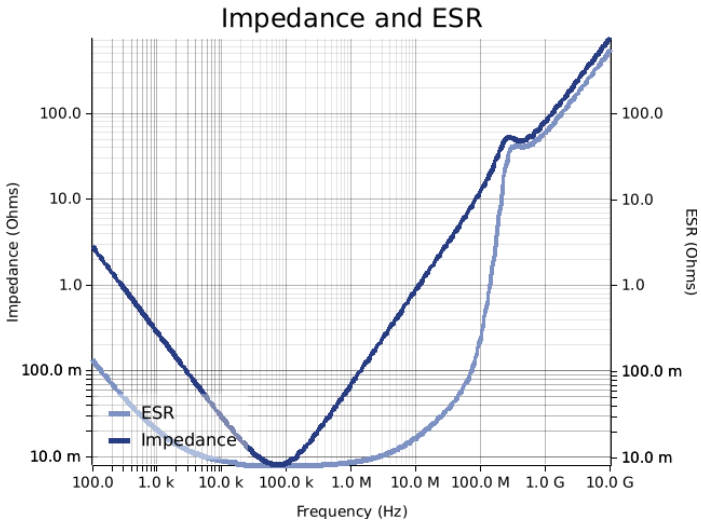
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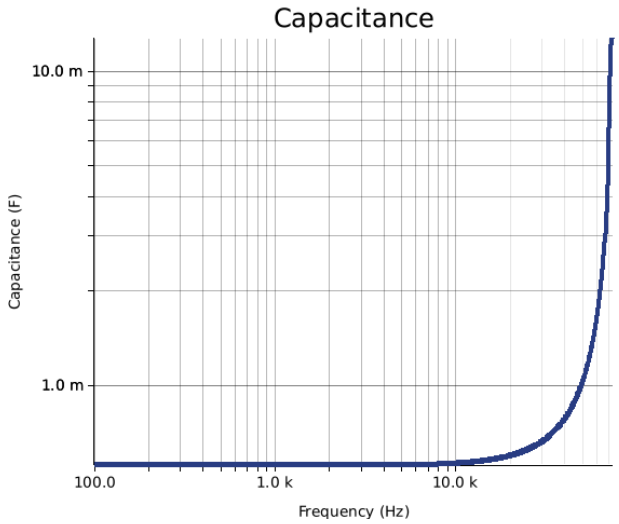
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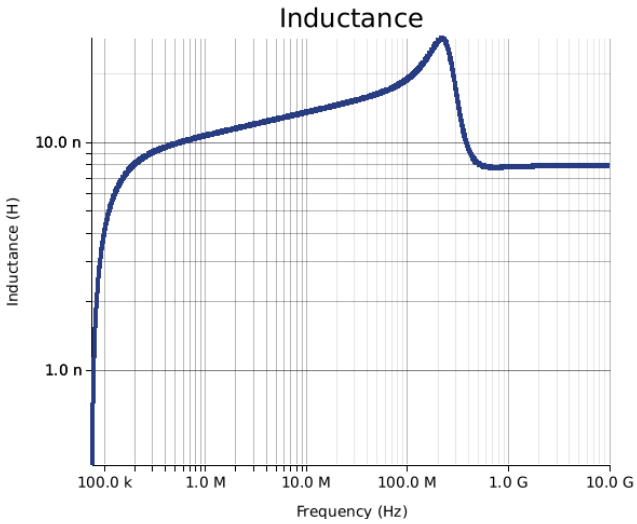
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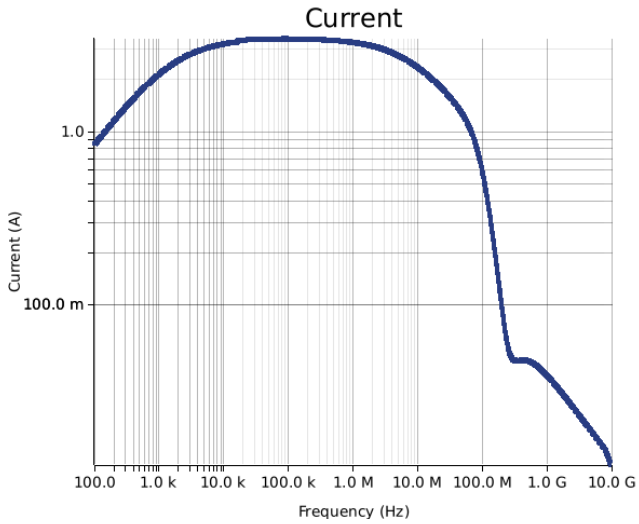
Simulations

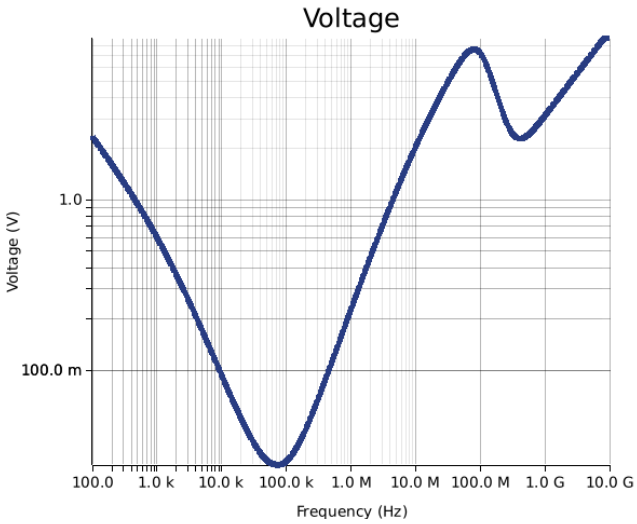
For the complete simulation environment please visit Y-SIM.

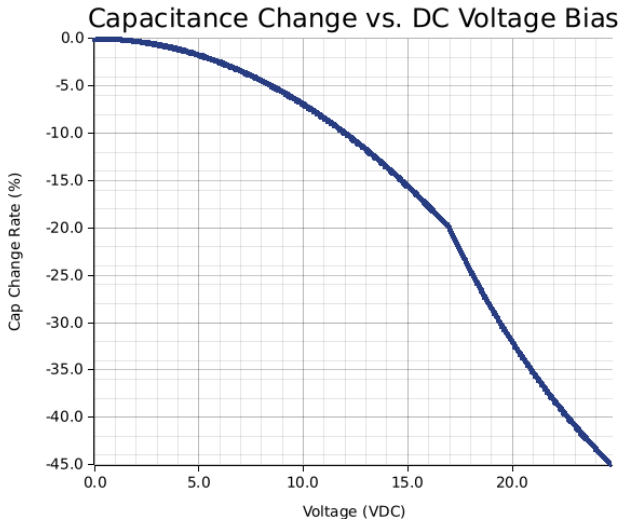








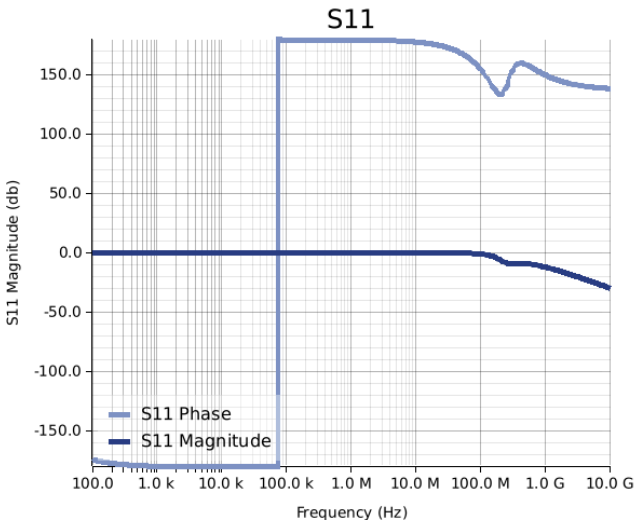


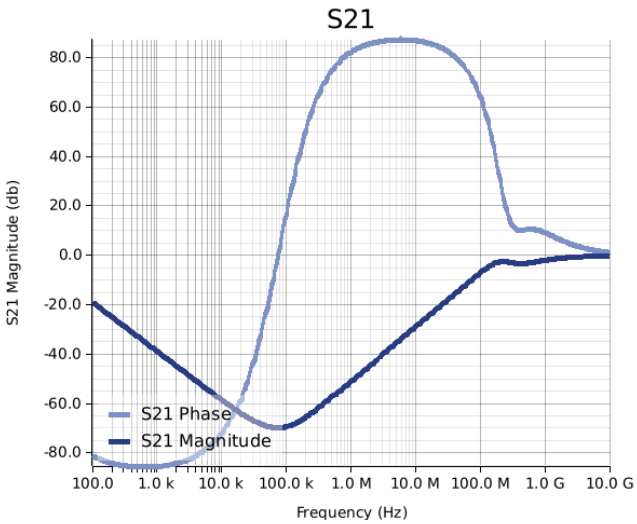


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These are simulations.
This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple “Ripple Current/Voltage vs. Frequency” plots is the ESR at ambient temperature.
- The ESR in the “Temperature Rise vs. Ripple Current” plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the “Temperature Rise vs. Combined Ripple Currents” plot are calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the “Information”) are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.

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